| FORM PTO-1449 | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | ATTY. DOCKET NO. 108391-00011 | | SERIAL NO. 09/652,023 |

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

| APPLICANT | Yasuhiro WAKIMOTO |
| FILING DATE August 31, 2000 | GROUP 2186 |

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
WAC	AA	5,802,554	9/01/98	CACERES et al			
WAC	AB	5,845,311	12/01/98	PIGUET et al			
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	NO	PART.
	AG								
	AH								
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	AK								
	AL								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

WAC	AM	"The 68040 On-Chip Memory Subsystem", Robin EDENFIELD et al, *Computer Society International Conference, Spring Meeting*, February 26, - March 2, 1990, Vol. Conf. 35, pgs. 264 - 269.
WAC	AN	"The DSP32C: AT&T's Second Generation Floating-Point Digital Signal Processor", Michael FUCCIO et al, *IEEE Micro*, December 1, 1988, Vol. 8, No. 6, pgs. 30 - 47.
	AO	BEST AVAILABLE COPY

EXAMINER	DATE CONSIDERED
	9/29/04

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.